

Roll No.

2206

B. E. (5th Semester) (ECE)

Examination – December, 2011

MICROPROCESSORS & INTER-FACING

Paper : EE - 309 - E

Time : Three hours]

[Maximum Marks : 100

Before answering the question, candidates should ensure that they have been supplied the correct and complete question paper. No complain in this regard, will be entertained after examination.

Note : Attempt *five* questions in all, taking at least *two* questions from each Part.

PART – A

1. (a) Explain architecture of 8085 microprocessor in detail. 12
(b) Explain various interrupts of 8085. 8

2. (a) Describe various instruction formats of 8086 microprocessor. 10

- (b) Write an assembly language program to find out sum of elements of an array. 10
3. (a) Describe the pin diagram of 8086 microprocessor. 10
- (b) How logical shift operations are different from arithmetic shift operations ? Explain any *five* shift operations and *five* branch instructions. 10
4. Write notes on :- 20
- (a) Basic differences in 8085 & 8086 microprocessor,
- (b) Flag register of 8086,
- (c) EU of 8086 .

PART – B

5. Explain architecture and various modes of 8255 PPI chip. 20
6. Explain DMA controller, its communication method, various signals and advantages in detail. 20
7. Explain pin diagram, architecture and functioning of 8259 interrupt controller. 20

8. Write notes on :

10 + 10 = 20

- (a) Control words of 8255 PPI chip,
 - (b) Programmable interval timer chips.
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